

	Document ID	Issue Date	Pages	Title	Current OR	Inventor
1	US 5062042 A	19911029	41	System for managing data which is accessible by file address or disk address via a disk track map	707/205	Binkley; Joseph H. et al.
2	US 5073968 A	19911217	12	Method and apparatus for marking emulation analysis states	703/26	Morrison; Robert D.
3	US 5088033 A	19920211	102	Data processing system emulation in a window with a coprocessor and I/O emulation	703/24	Binkley; Joseph H. et al.
4	US 5440710 A	19950808	19	Emulation of segment bounds checking using paging with sub-page validity	711/207	Richter; David E. et al.
5	US 5526503 A	19960611	17	Virtual addressing buffer circuit	711/203	Kim; Jason S.
6	US 5598553 A	19970128	18	Program watchpoint checking using paging with sub-page validity	703/23	Richter; David E. et al.
7	US 5737769 A	19980407	18	Physical memory optimization using programmable virtual address buffer circuits to redirect address requests	711/202	Kim; Jason Seung-Min
8	US 5809559 A	19980915	18	System and method utilizing a virtual addressing buffer circuit to emulate a device which is physically not present	711/202	Kim; Jason Seung-Min

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L17	147	emulat\$3 near2 memory near2 access\$3	USPAT	OR	OFF	2006/08/02 15:21
L18	0	L17 and ((emulat\$3 near2 memory near2 access\$3) with (valid adj address\$2))	USPAT	OR	OFF	2006/08/02 15:22
L19	8	L17 and (valid adj address\$2)	USPAT	OR	OFF	2006/08/02 15:22